TRAINING MASSIVE-SCALE AI FOUNDATIONAL MODELS ON FRONTIER

Sumanth Gudaparthi AMD Research and Advanced Development



Inelastic Neutron Spectroscopy (INS)







High Flux Isotope Reactor



LEADING THE EXASCALE ERA

- Powering World's #1 Supercomputer
 First to break Exascale barrier
- 9,408 Compute Nodes each with
 One 64-core AMD "Trento" CPU, four AMD Radeon Instinct MI250X
 GPUs, and 512GB of DDR4 memory
- 37,888 Instinct MI250X GPUs
 8,138,240 cores and 4.6 petabytes of HBM memory (128GB/GPU)
- 700 Petabytes of Storage
 9.95 Eflops on HPL-MxP Mixed-Precision Benchmark









AMD together we advance_



AMD together we advance_



Weather and Climate Predictions



 $\Delta t \approx hrs$



Discovery of new materials and new chemical processes



AMD together we advance_

DISTRIBUTED TRAINING OF LLMS ON FRONTIER

and and and and and and



WHAT DO YOU NEED TO TRAIN A TRILLION PARAMETER MODEL?

• Training Data Required^{*} = $(20 - 200) \times \#$ parameters

- = (20 200) × 1 Trillion
- = (20 200) Terabytes

 Total Compute Required = 6 × #parameters × # training_datapoints = 6 × (1 Trillion) × (20 – 200) Trillion = (120 – 1200) Million ExaFLOPS

Modern GPUs have < 200 GB Memory and operate at a few hundred of TFLOPS

Solution:

Distribute the training workload across thousands of GPUs on an ExaFLOP machine

*Source: Junqi Yin et. al, "FORGE: Pre-Training Open Foundation Models for Science". SC '23).

FRONTIER FOR THE RESCUE!

Frontier Specs

- 37,888 AMD Instinct[™] MI250X GPUs
- Each MI250X has
 - 128 GB memory
 - 383 TFLOPS peak throughput (FP16)
- Each MI250X is split across 2 GCDs (Graphic Compute Die)

Challenges:

- How to distribute the training workload across thousands of MI250X GPUs?
- How to take advantage of existing software developed for accelerating LLMs on these massive clusters?

3D PARALLELISM USING MEGATRON-DEEPSPEED

- A combination of Tensor, Pipeline, and Data parallelism ported to Frontier
- Determine how many GPUs (world-size) you need to fit the model
- Factorize world-size into TP (Tensor parallel size) and PP (Pipeline parallel size)

Distribution Strategy	Tunable Parameters
Tensor Parallelism	Tensor Parallel Size (TP)
Pipeline Parallelism	Pipeline Parallel Size (PP), #Microbatches (m)
Sharded Data Parallelism	ZeRO-1
Common	Micro Batch Size
Mixed Precision Training	FP16, BF16



Table: Distribution Strategies and Relevant Tunable Parameters

BEST PRACTICES WITH PARALLELISM PARADIGMS

- Tensor Parallelism (TP)
 - Keep it within the node (TP < 8)
- Pipeline Parallelism (PP)
 - Use large number of micro-batches (but that can increase the global batch-size)
- Data Parallelism (DP)
 - Can't use too much data parallelism. A large global batch size will make the model divergent



BEST PRACTICES WITH PARALLELISM PARADIGMS

- Tensor Parallelism (TP)
 - Keep it within the node (TP < 8)
- Pipeline Parallelism (PP)
 - Use large number of micro-batches (But that can increase the global batch-size)
- Data Parallelism (DP)
 - Can't use too much data parallelism. A large global batch size will make the model divergent.





AMD

together we advance_

BEST PRACTICES WITH PARALLELISM PARADIGMS

- Tensor Parallelism (TP)
 - Keep it within the node (TP < 8)
- Pipeline Parallelism (PP)
 - Use large number of micro-batches (But that can increase the global batch-size)
- Data Parallelism (DP)
 - Can't use too much data parallelism. A large global batch size will make the model divergent.





AMD

together we advance_

SEARCHING A 3D PARALLELISM STRATEGY USING DEEPHYPER



- DeepHyper: A Bayesian search algorithm for hyperparameter search.
- SHAP (SHapley Additive exPlanations) sensitivity analysis to assess the impact of hyperparameters on performance.
- Microbatch size is the most important parameter to tune followed by TP.

SEARCHING A 3D PARALLELISM STRATEGY USING DEEPHYPER



- DeepHyper: A Bayesian search algorithm for hyperparameter search.
- SHAP (SHapley Additive exPlanations) sensitivity analysis to assess the impact of hyperparameters on performance.
- Microbatch size is the most important parameter to tune followed by TP.

SCALING LARGE LANGUAGE MODELS SUMMARIZED

1. Training Challenges

- Training large language models (LLMs) with billions to trillions of parameters involves overcoming GPU memory and communication challenges.
- 2. Parallelism Strategies
 - Model parallelism (tensor and pipeline) and data parallelism distribute the load across multiple GPUs to address memory constraints.

3. Software and Frameworks

• The right combination of parallelization and frameworks like Megatron-DeepSpeed, plus hyperparameter tuning, are important to high throughput on Frontier with AMD ROCm software.

4. Performance Achievements

• Achieved high GPU throughput and strong scaling efficiencies (up to 100% weak scaling, 89% and 87% strong scaling) for 175 billion and 1 trillion parameter models on thousands of GPUs.

CLIMAX: AI FOUNDATION MODEL FOR BETTER WEATHER AND CLIMATE SOLUTIONS

Ward and the state and the



Clima

CLIMAX: VISION TRANSFORMER BACKBONE



Temperature

CLIMAX: VISION TRANSFORMER BACKBONE



AMD together we advance_

CLIMAX CAN ACCURATELY FORECAST WEATHER 72 HOURS AHEAD

Temperature



Wind



AMD together we advance_

CLIMAX: VISION TRANSFORMER BACKBONE



Temperature

Application Features

- High-resolution and highdimensional image data
- Complex spatial dependencies within images



CLIMAX: VISION TRANSFORMER BACKBONE



Temperature

Transformer Training Block

Application Features

- High-resolution and highdimensional image data
- Complex spatial dependencies within images

Implications

- More compute power
- Higher memory capacity
- Sophisticated processing for efficient scaling
- Complex parallelization strategies



CONVENTIONAL PARALLELIZATION STRATEGIES





• Each GPU works on a different data batch





Fully Sharded Data Parallel (FSDP)



• Each GPU works on a different data batch



CONVENTIONAL PARALLELIZATION STRATEGIES



Fully Sharded Data Parallel (FSDP)



- Each GPU works on a different data batch
- AllGather to bring in all the parameters before compute

CONVENTIONAL PARALLELIZATION STRATEGIES



Fully Sharded Data Parallel (FSDP)



- Each GPU works on a different data batch
- AllGather to bring in all the parameters before compute



- Each GPU works on a different data batch
- AllGather to bring in all the parameters before compute

AMD together we advance_

Each GPU works on partials of entire model



- Each GPU works on a different data batch
- AllGather to bring in all the parameters before compute

Each GPU works on partials of entire model





- Each GPU works on a different data batch
- AllGather to bring in all the parameters before compute

AMD together we advance_

Each GPU works on partials of entire model



- Each GPU works on a different data batch
- AllGather to bring in all the parameters before compute

Performance is limited by the peak memory use when gathering full model parameters

AllReduce to reduce all the partial activations together

Performance scalability is bottlenecked by the limited number of attention heads

MATRIX CHAIN MULTIPLICATION



MATRIX CHAIN MULTIPLICATION





























AMD together we advance_







AMD together we advance_



Fusing both FSDP and TP

 Does not need to gather a temporary copy of all parameters like FSDP => Lower peak memory footprint

 Scalability is not limited by the number of attention heads => Higher scalability



HYBRIDSTOP HIERARCHICAL PARALLELIZATION STRATEGY



• Each horizontal purple rectangle represents a tensor-parallel group.

HYBRIDSTOP HIERARCHICAL PARALLELIZATION STRATEGY



- Each horizontal purple rectangle represents a tensor-parallel group.
- Vertical red rectangles represent Fully Sharded Data Parallel (FSDP) groups.

HYBRIDSTOP HIERARCHICAL PARALLELIZATION STRATEGY





- Each horizontal purple rectangle represents a tensor-parallel group.
- Vertical red rectangles represent Fully Sharded Data Parallel (FSDP) groups.
- Green rectangles represent Distributed Data Parallel (DDP) groups.



Fusing both FSDP and TP

- Does not need to gather a temporary copy of all parameters like FSDP => Lower peak memory footprint
- Scalability is not limited by the number of attention heads => Higher scalability





Fusing both FSDP and TP

- Does not need to gather a temporary copy of all parameters like FSDP => Lower peak memory footprint
- Scalability is not limited by the number of attention heads => Higher scalability

*On 24,576 MI250X GCDs

SCALING VISION TRANSFORMERS SUMMARIZED

1. Complexity of Environmental Systems

• Predicting Earth system processes requires robust, adaptable, and scalable computational models due to their inherent complexity and numerous influencing variables.

2. Limitations of FSDP and TP

• FSDP is constrained by peak memory use during model gathering, and tensor parallelism is limited by attention heads.

3. Efficient Al Scaling

• Hybrid Sharded Tensor-Data Orthogonal Parallelism (HybridSTOP) retains 81-96% strong scaling efficiency at 24,576 GPUs, overcoming these limitations.

4. Broad Applicability

• The proposed techniques benefit fields with large datasets like astrophysics and biology, enhancing AI and HPC integration.



HYDRA-GNN: A SCALABLE GNN ARCHITECTURE FOR MATERIALS SCIENCE APPLICATIONS



GRAPH REPRESENTATIONS OF MATERIALS AT DIFFERENT SCALES

Atomic Scale



Mesoscale



Continuum Scale



Nodes = atoms Edges = interatomic bonds

Nodes = Voronoi centers Edges = connection between Voronoi centers Nodes = vertices of the finite element mesh Edges = edges of the finite element mesh

together we advance_

GNN ARCHITECTURE

The architecture of GNN is made of:

- 1. A graph embedding layer
- 2. Hidden graph layers capturing the short-range interactions between nodes
- 3. Pooling layers interleaved with graph layers
- 4. Fully connected (FC) dense layers

























EXTREME-SCALE HYPERPARAMETER OPTIMIZATION TRIALS

- Conduct hyperparameter trails (HPO) to determine the top-K best GNN configurations
- Run the top-K GNNs in an ensemble fashion
- 8,192 nodes of OLCF Frontier (87% of the machine) have been used to explore 200 unique hyperparameter configurations.



STRONG SCALING OF HYDRA-GNN ON FRONTIER



Small model ~ 54K parameters
 Medium model ~ 16M parameters
 Large model ~ 164M parameters

The suboptimal strong scaling could be a byproduct of load-imbalance in each individual GNN computecommunication pattern.



SCALING GRAPH NEURAL NETWORKS SUMMARIZED

1. Application Features

• Materials at different scaling (e.g., atomic scale, mesoscale) are represented as graphs and processed using graph neural networks.

2. Model Selection and Hyperparameter Optimization

• Conduct HPO trails and determine the top-K GNN hyperparameter configurations that result in the least mean absolute error. Run all the top-K GNN models in an ensemble fashion for the execution of the application.

3. Critical Properties of Hydra-GNN

• Hydra-GNN must satisfy five critical properties corresponding to flexibility, scalability, and heterogeneity in applications, software, and hardware support.

4. Performance Achievements

• Achieved high GPU throughput and strong scaling efficiencies up to few thousand GPUs.



CALL TO ACTION



CALL TO ACTION: GPU UTILIZATION

While our experiments show impressive strong and weak scaling, there is still opportunity for improvement

• GPU utilization is under 40% for all model sizes

Potential Opportunities

- Exploring opportunities to overlap communication with computations
- Efficient parallelization strategies that optimize data movement and memory accesses.
- Research novel attention algorithms

CALL TO ACTION: LOAD BALANCING

- Exploring load balancing opportunities while executing a single GNN model across multiple CUs / GPUs
- Exploring load balancing opportunities when running an ensemble of GNN architecture each with a different set of hyperparameters architecture, number of layer, FLOPs, etc.
- Run-time / dynamic memory allocation to improve GPU memory utilization

Load balancing of a single GNN model

Load balancing across multiple GNN models GPU memory utilization improvements

TAKEAWAYS

1. Significant Impact of Al-for-Science:

• Al-for-Science applications can significantly help better lives, economies, and communities.

2. Frontier's Role:

Frontier, our cutting-edge hardware, along with AMD ROCm software support, stands as the essential backbone, driving the
execution of these models with unmatched efficiency.

3. Three Foundational Models:

• We've categorized these applications into 3 foundational models, each tailored to specific computational and communication needs and challenges.

4. Challenges and Roadblocks

- GPU Compute and Memory Utilization: Optimizing the use of GPU resources remains a critical challenge.
- Load Balancing: Efficiently distributing workloads to maximize performance is another significant hurdle.

5. Future Directions

• We are working on more sophisticated tools to do nuanced analysis, characterization, and optimization of ML models at scale

ACKNOWLEDGEMENTS

- Ashwin Aji Organization: Advanced Micro Devices
- Karl W. Schulz Organization: Advanced Micro Devices
- Michael Schulte Organization: Advanced Micro Devices
- Austin Ellis
 Organization: Advanced Micro Devices

Jorda Polo

Organization: Advanced Micro Devices

- Angela Dalton Organization: Advanced Micro Devices
- Massimiliano Lupo Pasini Organization: ORNL
- Aristeidis Tsaris Organization: ORNL

- Leon Song Organization: Microsoft
- Sajal Dash Organization: ORNL

.

- Pei Zhang Organization: ORNL
- Jong Youl Choi Organization: ORNL

AMD

- Prasanna Balaprakash Organization: ORNL
- John Gounley Organization: ORNL
- Xiao Wang Organization: ORNL
- Kshitij Mehta Organization: ORNL
- **Dan Lu** Organization: ORNL

Relevant Sources

- 1. Sajal Dash et. al, "Optimizing Distributed Training on Frontier for Large Language Models", arXiv, 2023
- 2. T. Nguyen et. al, "Climax: A foundation model for weather and climate," 2023
- 3. Xiao Wang et. al, "ORBIT: Oak Ridge Base Foundation Model for Earth System Predictability", arXiv, 2024
- 4. "HydraGNN Distributed PyTorch implementation of multi-headed graph convolutional neural networks", Computing and Computational Sciences Directorate, Oak Ride National Laboratory
- 5. "HydraGNN: Distributed PyTorch implementation of multi-headed graph convolutional neural networks", Copyright ID#: 81929619 https://doi.org/10.11578/dc.20211019.2







Copyright and Disclaimer

©2024 Advanced Micro Devices, Inc. All rights reserved.

AMD, the AMD Arrow logo, EPYC, Ryzen, Instinct, V-Cache, Radeon, Infinity Fabric, CDNA, and combinations thereof are trademarks of Advanced Micro Devices, Inc. Other product names used in this publication are for identification purposes only and may be trademarks of their respective companies.

The information presented in this document is for informational purposes only and may contain technical inaccuracies, omissions, and typographical errors. The information contained herein is subject to change and may be rendered inaccurate releases, for many reasons, including but not limited to product and roadmap changes, component and motherboard version changes, new model and/or product differences between differing manufacturers, software changes, BIOS flashes, firmware upgrades, or the like. Any computer system has risks of security vulnerabilities that cannot be completely prevented or mitigated. AMD assumes no obligation to update or otherwise correct or revise this information. However, AMD reserves the right to revise this information and to make changes from time to time to the content hereof without obligation of AMD to notify any person of such revisions or changes.

THIS INFORMATION IS PROVIDED "AS IS." AMD MAKES NO REPRESENTATIONS OR WARRANTIES WITH RESPECT TO THE CONTENTS HEREOF AND ASSUMES NO RESPONSIBILITY FOR ANY INACCURACIES, ERRORS, OR OMISSIONS THAT MAY APPEAR IN THIS INFORMATION. AMD SPECIFICALLY DISCLAIMS ANY IMPLIED WARRANTIES OF NON-INFRINGEMENT, MERCHANTABILITY, OR FITNESS FOR ANY PARTICULAR PURPOSE. IN NO EVENT WILL AMD BE LIABLE TO ANY PERSON FOR ANY RELIANCE, DIRECT, INDIRECT, SPECIAL, OR OTHER CONSEQUENTIAL DAMAGES ARISING FROM THE USE OF ANY INFORMATION CONTAINED HEREIN, EVEN IF AMD IS EXPRESSLY ADVISED OF THE POSSIBILITY OF SUCH DAMAGES.